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Substitute for form 1449/PTO

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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

2 Sheet

Complete if known			
Application Number	10/700,429		
Filing Date	November 3, 2003		
First Named Inventor	Glenn J. Leedy		
Art Unit	2814		
Examiner Name	Shrinivas H. Rao		
Attorney Docket Number	ELM-1 Cont. 10		

U.S. PATENT DOCUMENTS					
Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Documents	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
	Number - Kind Code <sup>2</sup> (if known)				
	4,954,875	09-04-1990	Clements		
	5,481,133	01-02-1996	Hsu		
		Cite No. 1 Document Number    Number - Kind Code <sup>2</sup> (if known)   4,954,875	Document Number	Cite No. 1 Document Number Publication Date Mame of Patentee or Applicant of Cited Documents  4,954,875 09-04-1990 Clements	

	FOREIGN PATENT DOCUMENTS							
Examiner initials	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Documents	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T₫		
		Country Code 1 - Number 2 - Kind Code 1						
		EP 0238089 A2	09-23-87	Fujitsu Limited				
		EP 0703618 A1	03-27-96	FRAUNHOFER- GESELLSCHAFT ZUR FÖRDERUNG DER ANGEWANDTEN FORSCHUNG E.V.				
		EP 0703619 A1	03-27-96	FRAUNHOFER- GESELLSCHAFT ZUR FÖRDERUNG DER ANGEWANDTEN FORSCHUNG E.V.				
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Letter Office that issued the document, by the two-letter code (WIPO Standard ST.3).

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Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible.

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				First Named Inventor	Glenn J. Leedy	
			. 2.07	Art Unit	2814	
			necessary)	Examiner Name	Shrinivas H. Rao	
Sheet	2	of	2	Attorney Docket Number	ELM-1 Cont. 10	

	NON PATENT LITERATURE DOCUMENTS				
Examiner initials	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>6</sup>		
		Mitsumasa Koyanagi et al., "Design of 4-KBIT X 4-layer Optically Coupled Three-Dimensional Common Memory For Parallel Processor System," IEEE Journal of Solid-State Circuits, Vol. 25. No. 1, February 1, 1990.			